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/*
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 *
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 */

#include <common.h>
#include <command.h>
#include <cmd_boot.h>
#include <image.h>
#include <zlib.h>
#include <asm/byteorder.h>
#ifdef CONFIG_HAS_DATAFLASH
#include <dataflash.h>
#endif

#include <asm/setup.h>
#define tag_size(type) ((sizeof(struct tag_header) + sizeof(struct type)) >> 2)
#define tag_next(t) ((struct tag *)((u32 *) (t) + (t)->hdr.size))

#if defined (CONFIG_SETUP_MEMORY_TAGS) || \
    defined (CONFIG_CMDLINE_TAG) || \
    defined (CONFIG_INITRD_TAG) || \
    defined (CONFIG_VFD)
static void setup_start_tag(bd_t *bd);
# ifdef CONFIG_SETUP_MEMORY_TAGS
static void setup_memory_tags(bd_t *bd);
# endif
static void setup_commandline_tag(bd_t *bd, char *commandline);
# if 0
static void setup_ramdisk_tag(bd_t *bd);
# endif
# ifdef CONFIG_INITRD_TAG
static void setup_initrd_tag(bd_t *bd, ulong initrd_start, ulong initrd_end);
# endif
static void setup_end_tag(bd_t *bd);
# if defined (CONFIG_VFD)
static void setup_videolfb_tag(gd_t *gd);
# endif

static struct tag *params;
#endif /* CONFIG_SETUP_MEMORY_TAGS || CONFIG_CMDLINE_TAG || CONFIG_INITRD_TAG */

#ifdef CONFIG_SHOW_BOOT_PROGRESS
# include <status_led.h>
# define SHOW_BOOT_PROGRESS(arg) show_boot_progress(arg)
# else
# define SHOW_BOOT_PROGRESS(arg)
# endif

extern image_header_t header; /* from cmd_bootm.c */

/* ghcstop */
/*
 * armboot ( , arm cpu ) bootm linux
 * ppc common/cmd_bootm.c arm extern
 * jump .
 * ppc linux bd_t
 */
```

```

*      architecture id      .      ...
* ppc linux      가      arm      , u-boot
*      ....
*      armboot      u-boot & linux manual      denx      linuxarm
*
*      blob      ...^^
*
*      ...      arm
*      .(linuxarm      arch/arm/boot/Makefile      ...)
* -      (ppc405      ...2410      33000000      ...)
* ex> bootm 100000
* - ramdisk image
* ex> bootm 100000 200000
*/
void do_bootm_linux(cmd_tbl_t *cmdtp, int flag, int argc, char *argv[],
                    ulong addr, ulong *len_ptr, int verify)
{
    DECLARE_GLOBAL_DATA_PTR;

    ulong len = 0, checksum;
    ulong initrd_start, initrd_end;
    ulong data;
    void (*theKernel)(int zero, int arch);
    image_header_t *hdr = &header;
    bd_t *bd = gd->bd;
#ifdef CONFIG_CMDLINE_TAG
    char *commandline = getenv("bootargs");
#endif

    /*      header      entry point address      host byte
    theKernel      ....^^
    */
    theKernel = (void (*)(int, int))ntohl(hdr->ih_ep);

    /*
    * Check if there is an initrd image
    * : argument가      ramdisk image      .
    */
    if (argc >= 3) {
        SHOW_BOOT_PROGRESS (9);

        /*      argument      ...: ramdisk image가      loading      */
        addr = simple_strtoul(argv[2], NULL, 16);

        printf ("## Loading Ramdisk Image at %08lx ...\n", addr);

        /* Copy header so we can blank CRC field for re-calculation */
#ifdef CONFIG_HAS_DATAFLASH
        if (addr_dataflash(addr)){
            read_dataflash(addr, sizeof(image_header_t), (char *)&header);
        } else
#endif
#ifdef CONFIG_CMD_BOOTM
        /* cmd_bootm.c      global      header      ramdisk image header      가
        ...      ...^^ */
        memcpy (&header, (char *)addr, sizeof(image_header_t));

        /* magic number가      ...reset */
        if (ntohl(hdr->ih_magic) != IH_MAGIC) {
            printf ("Bad Magic Number\n");
            SHOW_BOOT_PROGRESS (-10);
            do_reset (cmdtp, flag, argc, argv); /* ==> cpu/arm920t/cpu.c */
        }

        /* ramdisk image header      checksum      ,      reset */
        data = (ulong)&header;
        len = sizeof(image_header_t);

        checksum = ntohl(hdr->ih_hcrc);
        hdr->ih_hcrc = 0;

        if (crc32 (0, (char *)data, len) != checksum) {
            printf ("Bad Header Checksum\n");
            SHOW_BOOT_PROGRESS (-11);
            do_reset (cmdtp, flag, argc, argv); /* ==> cpu/arm920t/cpu.c */
        }
#endif
    }
}

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    SHOW_BOOT_PROGRESS (10);

    /* header image          print: ==> common/cmd_bootm.c */
    print_image_hdr (hdr);

    /* header                ramdisk image                      */
    data = addr + sizeof(image_header_t);
    len  = ntohl(hdr->ih_size);

#ifdef CONFIG_HAS_DATAFLASH
    if (addr_dataflash(addr)){
        read_dataflash(data, len, (char *)CFG_LOAD_ADDR);
        data = CFG_LOAD_ADDR;
    }
#endif

    /* verify                1      0                      , do_bootm()
       1                      ramdisk image                checksum
       reset */
    if (verify) {
        ulong csum = 0;

        printf ("    Verifying Checksum ... ");
        csum = crc32 (0, (char *)data, len);
        if (csum != ntohl(hdr->ih_dcrc)) {
            printf ("Bad Data CRC\n");
            SHOW_BOOT_PROGRESS (-12);
            do_reset (cmdtp, flag, argc, argv);
        }
        printf ("OK\n");
    }

    SHOW_BOOT_PROGRESS (11);

    /* ramdisk image                      reset */
    if ((hdr->ih_os   != IH_OS_LINUX)      ||
        (hdr->ih_arch != IH_CPU_ARM)      ||
        (hdr->ih_type != IH_TYPE_RAMDISK) ) {
        printf ("No Linux ARM Ramdisk Image\n");
        SHOW_BOOT_PROGRESS (-13);
        do_reset (cmdtp, flag, argc, argv);
    }

    /*
     * Now check if we have a multifile image
     * : argument 7}                      ramdisk
     */
} else if ((hdr->ih_type==IH_TYPE_MULTI) && (len_ptr[1])) {
    ulong tail = ntohl(len_ptr[0]) % 4;
    int i;

    SHOW_BOOT_PROGRESS (13);

    /* skip kernel length and terminator */
    data = (ulong)&len_ptr[2];
    /* skip any additional image length fields */
    for (i=1; len_ptr[i]; ++i)
        data += 4;
    /* add kernel length, and align */
    data += ntohl(len_ptr[0]);
    if (tail) {
        data += 4 - tail;
    }

    len  = ntohl(len_ptr[1]);

} else {
    /*
     * no initrd image
     */
    SHOW_BOOT_PROGRESS (14);

    data = 0;
}
```

```
#ifdef DEBUG
    if (!data) {
        printf ("No initrd\n");
    }
#endif

/* ramdisk image가 ramdisk image */
if (data) {
    initrd_start = data;
    initrd_end = initrd_start + len;
} else { /* 0 ...*/
    initrd_start = 0;
    initrd_end = 0;
}

SHOW_BOOT_PROGRESS (15);

#ifdef DEBUG
    printf ("## Transferring control to Linux (at address %08lx) ...\n",
        (ulong)theKernel);
#endif

/*
(ghcstop_caution)
    kelp blob .

    initrd . ppc .
    parameter가 가 (tag)
    .

setup_start_tag()
params = (struct tag *)bd->bi_boot_params;
    bi_boot_params .

board/smdk2410/smdk2410.c board_init() gd->bd->bi_boot_params = 0x30000100;
    .

arch/arm/kernel/setup.c setup_arch() parse_tags():
    tag가 ( tag가 )
tag .

    s3c2410 architecture :
mizi arch/arm/mach-s3c2410/smdk.c
swl-patch arch/arm/mach-s3c2410/arch.c
    MACHINE_START MACHINE_END 가 BOOT_PARAMS()
    tag가 가 .
U-BOOT

board/smdk2410/smdk2410.c board_init() gd->bd->bi_boot_params = 0x30000100;
    . lib_arm/armlinux.c do_bootm_linux()
setup_start_tag()

*/
#if defined (CONFIG_SETUP_MEMORY_TAGS) || \
    defined (CONFIG_CMDLINE_TAG) || \
    defined (CONFIG_INITRD_TAG) || \
    defined (CONFIG_VFD)
    setup_start_tag(bd); /* bd parameter params
                        parameter bd */
#endif
#ifdef CONFIG_SETUP_MEMORY_TAGS
    setup_memory_tags(bd);
#endif
#ifdef CONFIG_CMDLINE_TAG
    setup_commandline_tag(bd, commandline);
#endif
#ifdef CONFIG_INITRD_TAG
    setup_initrd_tag(bd, initrd_start, initrd_end);
#endif
if 0
    setup_ramdisk_tag(bd);
#endif
```

```
#if defined (CONFIG_VFD)
    setup_videolfb_tag(gd);
#endif
setup_end_tag(bd);
#endif

/* we assume that the kernel is in place */
printf("\nStarting kernel ...\n\n");

/* ==> cpu/arm920t/cpu.c: I/D cache turn-off & I/D cache flush */
cleanup_before_linux();

/*
    argument r0 0 가 r1 architecture number가 . */
theKernel(0, bd->bi_arch_number);
}

#if defined (CONFIG_SETUP_MEMORY_TAGS) || \
    defined (CONFIG_CMDLINE_TAG) || \
    defined (CONFIG_INITRD_TAG) || \
    defined (CONFIG_VFD)
static void setup_start_tag(bd_t *bd)
{
    /*
     * board/smdk2410/smdk2410.c board_init()
     * gd->bd->bi_boot_params = 0x30000100;
     *
     */
    params = (struct tag *)bd->bi_boot_params;

    params->hdr.tag = ATAG_CORE;
    params->hdr.size = tag_size(tag_core);

    params->u.core.flags = 0;
    params->u.core.pagesize = 0;
    params->u.core.rootdev = 0;

    params = tag_next(params);
}

#ifdef CONFIG_SETUP_MEMORY_TAGS
static void setup_memory_tags(bd_t *bd)
{
    int i;

    for(i = 0; i < CONFIG_NR_DRAM_BANKS; i++) {
        params->hdr.tag = ATAG_MEM;
        params->hdr.size = tag_size(tag_mem32);

        params->u.mem.start = bd->bi_dram[i].start;
        params->u.mem.size = bd->bi_dram[i].size;

        params = tag_next(params);
    }
}
#endif /* CONFIG_SETUP_MEMORY_TAGS */

static void setup_commandline_tag(bd_t *bd, char *commandline)
{
    char *p;

    /* eat leading white space */
    for(p = commandline; *p == ' '; p++)
        ;

    /* skip non-existent command lines so the kernel will still
     * use its default command line.
     */
    if(*p == '\0')
        return;
}
```

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    params->hdr.tag = ATAG_CMDLINE;
    params->hdr.size = (sizeof(struct tag_header) + strlen(p) + 1 + 4) >> 2;

    strcpy(params->u.cmdline.cmdline, p);

    params = tag_next(params);
}

#ifdef ATAG_INITRD2
#define ATAG_INITRD2    0x54420005
#endif

#ifdef CONFIG_INITRD_TAG
static void setup_initrd_tag(bd_t *bd, ulong initrd_start, ulong initrd_end)
{
    /* an ATAG_INITRD node tells the kernel where the compressed
     * ramdisk can be found. ATAG_RDIMG is a better name, actually.
     */
    params->hdr.tag = ATAG_INITRD2;
    params->hdr.size = tag_size(tag_initrd);

    params->u.initrd.start = initrd_start;
    params->u.initrd.size = initrd_end - initrd_start;

    params = tag_next(params);
}
#endif /* CONFIG_INITRD_TAG */

#if 0
static void setup_ramdisk_tag(bd_t *bd)
{
    /* an ATAG_RAMDISK node tells the kernel how large the
     * decompressed ramdisk will become.
     */
    params->hdr.tag = ATAG_RAMDISK;
    params->hdr.size = tag_size(tag_ramdisk);

    params->u.ramdisk.start = 0;
    /*params->u.ramdisk.size = RAMDISK_SIZE; */
    params->u.ramdisk.flags = 1; /* automatically load ramdisk */

    params = tag_next(params);
}
#endif /* 0 */

#if defined (CONFIG_VFD)
static void setup_videolfb_tag(gd_t *gd)
{
    /* An ATAG_VIDEOLFB node tells the kernel where and how large
     * the framebuffer for video was allocated (among other things).
     * Note that a _physical_ address is passed !
     *
     * We only use it to pass the address and size, the other entries
     * in the tag_videolfb are not of interest.
     */
    params->hdr.tag = ATAG_VIDEOLFB;
    params->hdr.size = tag_size(tag_videolfb);

    params->u.videolfb.lfb_base = (u32)gd->fb_base;
    /* 7168 = 256*4*56/8 - actually 2 pages (8192 bytes) are allocated */
    params->u.videolfb.lfb_size = 7168;

    params = tag_next(params);
}
#endif

static void setup_end_tag(bd_t *bd)
{
    params->hdr.tag = ATAG_NONE;
    params->hdr.size = 0;
}

#endif /* CONFIG_SETUP_MEMORY_TAGS || CONFIG_CMDLINE_TAG || CONFIG_INITRD_TAG */

```