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1  /*
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4  *
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8  *
9  * See file CREDITS for list of people who contributed to this
10 * project.
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23 * along with this program; if not, write to the Free Software
24 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
25 * MA 02111-1307 USA
26 */
27
28 #include <common.h>
29 #include <command.h>
30 #include <malloc.h>
31 #include <devices.h>
32 #include <version.h>
33 #include <net.h>
34
35 #if (CONFIG_COMMANDS & CFG_CMD_NAND)
36 void nand_init (void);
37 #endif
38
39 ulong monitor_flash_len;
40
41 #ifdef CONFIG_HAS_DATAFLASH
42 extern int AT91F_DataflashInit(void);
43 extern void dataflash_print_info(void);
44 #endif
45
46 #ifndef CONFIG_IDENT_STRING
47 #define CONFIG_IDENT_STRING ""
48 #endif
49
50 const char version_string[] =
51     U_BOOT_VERSION " (" __DATE__ " - " __TIME__ ") " CONFIG_IDENT_STRING;
52
53 #ifdef CONFIG_DRIVER_CS8900
54 extern void cs8900_get_enetaddr (uchar * addr);
55 #endif
56
57 #ifdef CONFIG_DRIVER_CS8900
58 extern void rtl8019_get_enetaddr (uchar * addr);
59 #endif
60
61 #ifdef CONFIG_DRIVER_LAN91C96
62 #include "../drivers/lan91c96.h"
63 #endif
64
65
66 /* ghcstop */
67 /*
68 * Begin and End of memory area for malloc(), and current "brk"
69 */
70 static ulong mem_malloc_start = 0;
71 static ulong mem_malloc_end = 0;
72 static ulong mem_malloc_brk = 0;
73
74 /*
75 * cpu/arm920t/start.S
76 * _armboot_real_end is the first usable RAM address behind armboot
77 * and the various stacks
78 _armboot_real_end   armboot           가           가   ram address가
79   . start_armboot           cpu/arm920t/cpu.c   cpu_init()   .
80   armboot               .
81 _armboot_real_end = _armboot_end + CONFIG_STACKSIZE;
82

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83         dest_addr  _armboot_real_end  .
84
85     malloc      .      malloc()
86     ,          malloc      brk      ....^^
87 */
88 static
89 void mem_malloc_init (ulong dest_addr)
90 {
91     mem_malloc_start = dest_addr;
92     mem_malloc_end = dest_addr + CFG_MALLOC_LEN;
93     mem_malloc_brk = mem_malloc_start;
94
95     memset ((void *) mem_malloc_start, 0,
96             mem_malloc_end - mem_malloc_start);
97 }
98
99 void *sbrk (ptrdiff_t increment)
100 {
101     ulong old = mem_malloc_brk;
102     ulong new = old + increment;
103
104     if ((new < mem_malloc_start) || (new > mem_malloc_end)) {
105         return (NULL);
106     }
107     mem_malloc_brk = new;
108
109     return ((void *) old);
110 }
111
112 /*****
113  * Init Utilities
114  *****/
115 * Some of this code should be moved into the core functions,
116 * or dropped completely,
117 * but let's get it working (again) first...
118 */
119 /* ghcstop */
120 static int init_baudrate (void)
121 {
122     DECLARE_GLOBAL_DATA_PTR;
123
124     uchar tmp[64]; /* long enough for environment variables */
125     /*
126      * board.c,          env_ptr->data      default_environment[]
127      * baudrate          gd
128      */
129     int i = getenv_r ("baudrate", tmp, sizeof (tmp));
130
131     /*
132      * character( ...)      가 0
133      * , board configuration header file  CONFIG_BAUDRATE
134      * )                  CONFIG_BAUDRATE  board
135      * description structure
136      */
137
138     gd->bd->bi_baudrate = gd->baudrate = (i > 0)
139         ? (int) simple_strtoul (tmp, NULL, 10)
140         : CONFIG_BAUDRATE;
141
142     return (0);
143 }
144
145 static int display_banner (void)
146 {
147     printf ("\n\n%s\n\n", version_string);
148     printf ("U-Boot code: %08lx -> %08lx BSS: -> %08lx\n",
149             _armboot_start, _bss_start, _bss_end);
150 #ifdef CONFIG_MODEM_SUPPORT
151     puts ("Modem Support enabled\n");
152 #endif
153 #ifdef CONFIG_USE_IRQ
154     printf ("IRQ Stack: %08lx\n", IRQ_STACK_START);
155     printf ("FIQ Stack: %08lx\n", FIQ_STACK_START);
156 #endif
157
158     return (0);
159 }
160
161 /*
162  * WARNING: this code looks "cleaner" than the PowerPC version, but
163  * has the disadvantage that you either get nothing, or everything.
164  * On PowerPC, you might see "DRAM: " before the system hangs - which

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165  * gives a simple yet clear indication which part of the
166  * initialization if failing.
167  */
168  static int display_dram_config (void)
169  {
170      DECLARE_GLOBAL_DATA_PTR;
171      int i;
172
173      puts ("RAM Configuration:\n");
174
175      for(i=0; i<CONFIG_NR_DRAM_BANKS; i++) {
176          printf ("Bank #d: %08lx ", i, gd->bd->bi_dram[i].start);
177          print_size (gd->bd->bi_dram[i].size, "\n");
178      }
179
180      return (0);
181  }
182
183  static void display_flash_config (ulong size)
184  {
185      puts ("Flash: ");
186      print_size (size, "\n");
187  }
188
189  /*
190  * Breathe some life into the board...
191  *
192  * Initialize a serial port as console, and carry out some hardware
193  * tests.
194  *
195  * The first part of initialization is running from Flash memory;
196  * its main purpose is to initialize the RAM so that we
197  * can relocate the monitor code to RAM.
198  */
199
200
201  /*
202  * All attempts to come up with a "common" initialization sequence
203  * that works for all boards and architectures failed: some of the
204  * requirements are just _too_ different. To get rid of the resulting
205  * mess of board dependent #ifdef'ed code we now make the whole
206  * initialization sequence configurable to the user.
207  *
208  * The requirements for any new initialization function is simple: it
209  * receives a pointer to the "global data" structure as it's only
210  * argument, and returns an integer return code, where 0 means
211  * "continue" and != 0 means "fatal error, hang the system".
212  */
213  typedef int (init_fnc_t) (void);
214
215  /* ghcstop */
216  init_fnc_t *init_sequence[] = {
217      cpu_init,          /* basic cpu dependent setup */ /* => cpu/arm920t/cpu.c */
218      board_init,        /* basic board dependent setup */ /* => board/smdk2410/smdk2410.c */
219      interrupt_init,     /* setup internal timer: */ /* => cpu/arm920t/interrupts.c */
220      /*
221       * CFG_ENV_IS_IN_FLASH (smdk2410.h )
222       * common/env_flash.c env_init()가 ...
223       * CFG_ENV_ADDR_REDUND smdk2410.h 가
224       * CFG_ENV_ADDR_REDUND dual env... .
225       * ....
226       * ==> common/env_flash.c env_init() jump...^^
227       */
228      env_init,          /* initialize environment */
229      /*
230       * board.c, env_ptr->data default_environment[]
231       * baudrate gd .
232       */
233      init_baudrate,     /* initialize baudrate settings */
234      /* ==> cpu/arm920t/serial.c */
235      serial_init,       /* serial communications setup */
236      /*
237       * ==> common/console.c: global data
238       * structure gd->have_console = 1; .
239       */
240      console_init_f,    /* stage 1 init of console */
241      /* ... */
242      display_banner,    /* say that we are here */
243  };
244
245
246

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247     /* ==> board/smdk2410/smdk2410.c */
248     dram_init,      /* configure available RAM banks */
249
250     /* board.c          ....dram          . */
251     display_dram_config,
252     #if defined(CONFIG_VCMA9)
253     checkboard,
254     #endif
255     NULL,
256 };
257
258 void start_armboot (void)
259 {
260     /*
261     * include/asm-arm/global_data.h
262     * #define DECLARE_GLOBAL_DATA_PTR      register gd_t *gd asm ("r8")
263     * gd_t          gd      r8          register          (?).      stack
264     *
265     */
266     DECLARE_GLOBAL_DATA_PTR;
267
268     ulong size;
269     init_fnc_ptr **init_fnc_ptr; // ( , 가 가 ...),
270     char *s;
271     #if defined(CONFIG_VFD) // lcd controller
272     unsigned long addr;
273     #endif
274
275     //          memory clear
276     /* Pointer is writable since we allocated a register for it */
277     gd = (gd_t*)(_armboot_start - CFG_MALLOC_LEN - sizeof(gd_t));
278     memset ((void*)gd, 0, sizeof (gd_t));
279     gd->bd = (bd_t*)((char*)gd - sizeof(bd_t));
280     memset (gd->bd, 0, sizeof (bd_t));
281
282     monitor_flash_len = _bss_start - _armboot_start;
283
284     //          ...          structure          .
285     for (init_fnc_ptr = init_sequence; *init_fnc_ptr; ++init_fnc_ptr) {
286         if ((*init_fnc_ptr)() != 0) {
287             hang ();
288         }
289     }
290
291     /* configure available FLASH banks */
292
293     /* ==> board/smdk2410/flash.c: flash          flash
294     * sector          ,          return          ...
295     * 2410      amd      intel strata flash가          ,
296     *          가          ,          amd
297     *          .          amd          .
298     */
299     size = flash_init ();
300
301     /* board.c : flash size          */
302     display_flash_config (size);
303
304     /*
305     * LCD      define      board/trap/vfd.c          ...      2400      LCD controller
306     *
307     * drv_vfd_init(): initialize LCD-Controller of the S3C2400 for using VFDs
308     */
309     #ifndef CONFIG_VFD
310     #   ifndef PAGE_SIZE
311     #       define PAGE_SIZE 4096
312     #   endif
313     /*
314     * reserve memory for VFD display (always full pages)
315     */
316     /* armboot_end is defined in the board-specific linker script */
317     addr = (_bss_start + (PAGE_SIZE - 1)) & ~(PAGE_SIZE - 1);
318     size = vfd_setmem (addr);
319     gd->fb_base = addr;
320     #endif /* CONFIG_VFD */
321     /*
322     * malloc          . ram      armboot      가          .
323     *
324     */
325     /* armboot_start is defined in the board-specific linker script */
326     mem_malloc_init (_armboot_start - CFG_MALLOC_LEN);
327
328     #if (CONFIG_COMMANDS & CFG_CMD_NAND)

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329     puts ("NAND:");
330     nand_init();          /* go init the NAND */
331 #endif
332
333 #ifdef CONFIG_HAS_DATAFLASH
334     AT91F_DataflashInit();
335     dataflash_print_info();
336 #endif
337
338     /*
339     * initialize environment ==> common/env_common.c
340     * gd->reloc_off      offset
341     * (      0          )
342     * env      가      (valid가      )
343     * default_environment[] env      가      가
344     *      ...          ...
345     */
346     env_relocate ();
347
348 #ifdef CONFIG_VFD
349     /* must do this after the framebuffer is allocated */
350     drv_vfd_init();
351 #endif /* CONFIG_VFD */
352
353     /* IP Address: string      (ipaddr=...)      가
354     network order byte 4      . string      hex
355     board description structure .*/
356     gd->bd->bi_ip_addr = getenv_IPAddr ("ipaddr");
357
358     /* MAC Address:      ipaddr      가      . */
359     {
360         int i;
361         ulong reg;
362         char *s, *e;
363         uchar tmp[64];
364
365         i = getenv_r ("ethaddr", tmp, sizeof (tmp));
366         s = (i > 0) ? tmp : NULL;
367
368         for (reg = 0; reg < 6; ++reg) {
369             gd->bd->bi_enetaddr[reg] = s ? simple_strtoul (s, &e, 16) : 0;
370             if (s)
371                 s = (*e) ? e + 1 : e;
372         }
373     }
374
375     /* common/devices.c: device linked list      serial device
376     stdio offset reloc_off      가      ...
377     reloc_off가 0      ...
378     가 2 ptr      ...
379     가      ...
380     가      ...
381     가      ...
382     */
383     devices_init (); /* get the devices list going. */
384
385     jumptable_init ();
386
387     /* common/console.c: stdout, stdin, stderr out,in device      */
388     console_init_r (); /* fully init console as a device */
389
390 #if defined(CONFIG_MISC_INIT_R)
391     /* miscellaneous platform dependent initialisations */
392     misc_init_r ();
393 #endif
394
395     /* cpu/arm920t/interrupts.c
396     * enable exceptions
397     * CONFIG_USE_IRQ      return .
398     * smdk2410      include/configs/smdk2410.h #undef
399     *      return .
400     */
401     /* enable exceptions */
402     enable_interrupts ();
403
404     /*
405     drivers/cs8900.c
406     smdk2410      ...
407     , smdk2410.h      define .
408     mac address가
409     environment      mac address      cs8900 eeprom
410     environment      environment      ,

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411
412     eeprom
413     */
414     /* Perform network card initialisation if necessary */
415 #ifdef CONFIG_DRIVER_CS8900
416     cs8900_get_enetaddr (gd->bd->bi_enetaddr);
417 #endif
418
419 #ifdef CONFIG_DRIVER_LAN91C96
420     if (getenv ("ethaddr")) {
421         smc_set_mac_addr(gd->bd->bi_enetaddr);
422     }
423     /* eth_hw_init(); */
424 #endif /* CONFIG_DRIVER_LAN91C96 */
425
426     /* Initialize from environment: default image load , tftp
427     ... .....405 */
428     if ((s = getenv ("loadaddr")) != NULL) {
429         load_addr = simple_strtoul (s, NULL, 16);
430     }
431 #if (CONFIG_COMMANDS & CFG_CMD_NET)
432     if ((s = getenv ("bootfile")) != NULL) {
433         copy_filename (BootFile, s, sizeof (BootFile));
434     }
435 #endif /* CFG_CMD_NET */
436
437     /* ... */
438 #ifdef BOARD_LATE_INIT
439     board_late_init ();
440 #endif
441
442     /*
443     *
444     * : autoboot .....==>common/main.c
445     * main_loop
446     *
447     * 가 bootm
448     * common/cmd_bootm.c do_bootm()
449     * do_bootm
450     * do_bootm_linux() ARM cmd_bootm.c do_bootm_linux()
451     * 가 ppc
452     * ARM do_bootm_linux() lib_arm/armlinux.c
453     */
454     /* main_loop() can return to retry autoboot, if so just run it again. */
455     for (;;) {
456         main_loop ();
457     }
458     /* NOTREACHED - no way out of command loop except booting */
459 }
460
461 void hang (void)
462 {
463     puts ("### ERROR ### Please RESET the board ###\n");
464     for (;;) ;
465 }
466
467 #ifdef CONFIG_MODEM_SUPPORT
468 /* called from main loop (common/main.c) */
469 extern void dbg(const char *fmt, ...);
470 int mdm_init (void)
471 {
472     char env_str[16];
473     char *init_str;
474     int i;
475     extern char console_buffer[];
476     static inline void mdm_readline(char *buf, int bufsiz);
477     extern void enable_putc(void);
478     extern int hwflow_onoff(int);
479
480     enable_putc(); /* enable serial_putc() */
481
482 #ifdef CONFIG_HWFLOW
483     init_str = getenv("mdm_flow_control");
484     if (init_str && (strcmp(init_str, "rts/cts") == 0))
485         hwflow_onoff (1);
486     else
487         hwflow_onoff(-1);
488 #endif
489
490     for (i = 1; i++; ) {
491         sprintf(env_str, "mdm_init%d", i);
492         if ((init_str = getenv(env_str)) != NULL) {

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```
493     serial_puts(init_str);
494     serial_puts("\n");
495     for(;;) {
496         mdm_readline(console_buffer, CFG_CBSIZE);
497         dbg("ini%d: [%s]", i, console_buffer);
498
499         if ((strcmp(console_buffer, "OK") == 0) ||
500             (strcmp(console_buffer, "ERROR") == 0)) {
501             dbg("ini%d: cmd done", i);
502             break;
503         } else /* in case we are originating call ... */
504             if (strcmp(console_buffer, "CONNECT", 7) == 0) {
505                 dbg("ini%d: connect", i);
506                 return 0;
507             }
508     }
509     } else
510         break; /* no init string - stop modem init */
511
512     udelay(100000);
513 }
514
515 udelay(100000);
516
517 /* final stage - wait for connect */
518 for(;i > 1;) { /* if 'i' > 1 - wait for connection
519     message from modem */
520     mdm_readline(console_buffer, CFG_CBSIZE);
521     dbg("ini_f: [%s]", console_buffer);
522     if (strcmp(console_buffer, "CONNECT", 7) == 0) {
523         dbg("ini_f: connected");
524         return 0;
525     }
526 }
527
528 return 0;
529 }
530
531 /* 'inline' - We have to do it fast */
532 static inline void mdm_readline(char *buf, int bufsiz)
533 {
534     char c;
535     char *p;
536     int n;
537
538     n = 0;
539     p = buf;
540     for(;;) {
541         c = serial_getc();
542
543         /*      dbg("(c)", c); */
544
545         switch(c) {
546             case '\r':
547                 break;
548             case '\n':
549                 *p = '\0';
550                 return;
551
552             default:
553                 if(n++ > bufsiz) {
554                     *p = '\0';
555                     return; /* sanity check */
556                 }
557                 *p = c;
558                 p++;
559                 break;
560         }
561     }
562 }
563 #endif /* CONFIG_MODEM_SUPPORT */
564
```